

Figs. 3B, 4B, 5B and 6B are sectional views of the TFT array panel shown in Figs. 3A, 4A, 5A and 6A taken along the lines IIIB-IIIB', IVB-IVB', VB-VB' and VIB-VIB', respectively;

5 Fig. 7 is a layout view of an exemplary TFT array panel for an LCD according to another embodiment of the present invention;

Figs. 8 and 9 are sectional views of the TFT array panel shown in Fig. 7 taken along the lines VIII-VIII' and IX-IX', respectively;

10 Fig. 10A is a layout view of a TFT array panel shown in Figs. 7-9 in the first step of a manufacturing method thereof according to an embodiment of the present invention;

Figs. 10B and 10C are sectional views of the TFT array panel shown in Fig. 10A taken along the lines XB-XB' and XC-XC', respectively;

15 Figs. 11A and 11B are sectional views of the TFT array panel shown in Fig. 10A taken along the lines XB-XB' and XC-XC', respectively, and illustrate the step following the step shown in Figs. 10B and 10C;

Fig. 12A is a layout view of the TFT array panel in the step following the step shown in Figs. 11A and 11B;

Figs. 12B and 12C are sectional views of the TFT array panel shown in Fig. 12A taken along the lines XIIB-XIIB' and XIIC-XIIC', respectively;

RG 9/28/06 20 Figs. 13A, 14A and 15A, and Figs. 13B, 14B and ~~14B~~^{15B} are respective sectional views of the TFT array panel shown in Fig. 12A taken along the lines XIIB-XIIB' and XIIC-XIIC', respectively, and illustrate the steps following the step shown in Figs. 12B and 12C;

25 Fig. 16A is a layout view of a TFT array panel in the step following the step shown in Figs. 15A and 15B; and

Figs. 16B and 16C are sectional views of the TFT array panel shown in Fig. 16A taken along the lines XVIB-XVIB' and XVIC-XVIC', respectively;